ABSTRACT

MATRIX DISPLAY AND METHOD OF DRIVING A MATRIX DISPLAY

5   A display component includes in each pixel of a block a summing element, such as a capacitor (26), current source (22) and current sink (32), and switches (24,34) connecting the current source and sink to the summing element (32). Basis functions are supplied to basis function inputs (42, 44) to control the switches (24,34) in accordance with the basis functions. The

10   current source (22) and sink (32) of the pixels of the block are modulated in common in accordance with an input data stream. Decoded transform data is accumulated on capacitor (26), the display output being determined by the accumulated voltage. The individual pixels are thus able to carry out a data decoding operation.

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[Figure 3]